Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1123095	(semiconductor adj3 devic$2) or (integrated adj3 circuit$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/17 13:16
L2	36362	L1 same wafer same substrat$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/17 13:16
L3	372	L2 same (scrib$3 adj4 (lane or lin$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/17 13:16
L4	371	L2 same (scrib$3 adj3 (lane or lin$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/17 13:16
L5	126	L4 same (channel or connect$4 or inter$1connect$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/17 13:16
L6	7	5 and "365"/$7.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/17 13:17
L7	122	L4 same (channel or connect$3 or inter$1connect$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/17 13:17
L8	64	L7 same (separat$4 or divid$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/17 13:17

L9	5	7 and 365/51,63.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/17 13:18
L10	2040	L1 same master same slav$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/17 13:18
L11	16	L10 and (scrib$2 adj3 (lin$2 or lan$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/17 13:18
L12	14	L11 and (transceiver$2 or inverter)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/17 13:18
L13	0	((integrated adj circuit adj3 package) and (semiconductor adj substrate) and ((set or double) adj semiconductor) and (interior adj scrib$2 adj3 (lane or line)) and (inter$1connet$3 adj channel$3)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/17 13:21
L14	0	((integrated adj circuit adj3 package) and (semiconductor adj substrate) and ((set or double) adj semiconductor) and (interior adj scrib$2 adj3 (lane or line)) and (bond or pad) and wire and (inter$1connet$3 adj channel$3)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/17 13:22
L15	0	((method adj inter$1connet$3) and (integrated adj circuit) and function$3 and wafer and (inter$1connect$3 adj channel) and (scirb$2 adj (lane or line)) and (cut or cutt$3)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/17 13:24
S45	1068807	(semiconductor adj3 devic$2) or (integrated adj3 circuit$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/01 09:22

S46	33196	S45 same wafer same substrat$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/01 09:24
S47	348	S46 same (scrib$3 adj3 (lane or lin$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/17 13:16
S48	115	S47 same (channel or connect$3 or inter$1connect$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/17 13:16
S49	60	S48 same (separat$3 or divid$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/17 13:17
S50	7	S48 and "365"/$7.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/17 13:17
S51	5	S47 and 365/51,63.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/17 13:18
S52	8	gamini-nader.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/01 10:17
S53	0	gamini-nader-$.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/01 10:17

S54	49	perino-donald-v.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/01 10:17
S55	3	perino-donald.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/01 10:17
S56	53	perino-donald-$.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/01 10:17
S57	7	"6583035"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/01 10:18
S58	18	"6404660"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/01 10:18
S59	5	"6714431"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/01 10:18
S60	1934	S45 same master same slav$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/01 10:19
S61	189	S60 same modul$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/01 10:20

S62	15	S61 and (scrib$2 adj3 (lin$2 or lan$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/01 10:21
S63	15	S60 and (scrib$2 adj3 (lin$2 or lan$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/01 10:21
S64	13	S63 and (transceiver or inverter)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/17 13:18
S65	7	S64 and substrat$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/01 10:22
S66	11	S61 and "365"/$7.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/01 10:24